

PROGRAM OBJECTIVE

As per the mission of Government of India, development of smart cities and villages, Digital India etc. are the need of a day.

This training program is mainly focused on the techniques like computer vision, pattern recognition, machine learning and deep learning. It is aimed to train the faculties to directive their students/researchers to fulfill the mission of government.

The training program aims to provide exposure to:

- Introduction of basic concepts of pattern recognition, machine learning and deep learning.
- Introduction to ANN and CNN.
- Deep learning for multiple data types.
- Deep learning using CUDA.
- Pattern recognition applications using deep learning techniques and Tensor flow.

ELIGIBILITY

The program is open to the faculty members of engineering colleges, research scholars and other allied disciplines. Industry personal working in the same discipline can also attend the program.

PRE REGISTRATION

Kindly fill up required details on below link for pre-registration. Pre-registration is mandatory.

Link: <http://bit.ly/2Scv3ti>

Last date for Pre-registration:

30th September, 2019

PROFORMA FOR REGISTRATION FORM

Online Reg. Ref. No. : _____

Name: (In Block Letters) _____

Gender: _____

Designation: _____

Institutional Address: _____

Highest Qualification: _____

Area of Specialization: _____

Research Interests: _____

Accommodation required: YES/NO

Address for Correspondence: _____

Mobile No. _____

Email: _____

Demand Draft No: _____ drawn on

_____ dated _____

Date: _____ **Signature of Applicant**

The candidate Mr./Mrs./Dr./Prof. _____

is hereby officially permitted by institute to attend the STTP titled "**Deep Learning, Machine Learning and Pattern Recognition**" during the period 24th to 29th NOV 2019.

SIGNATURE OF DIRECTOR / PRINCIPAL with SEAL

Sarvajanik Education Society
**Sarvajanik College of Engineering and
Technology**

**AICTE SPONSORED ONE WEEK
SHORT TERM TRAINING
PROGRAM
on**

**Deep Learning, Machine Learning and
Pattern Recognition**

24th to 29th NOV. 2019

Organized by



**Department of Electronics and
Communication Engineering**

(Re-Accredited by National Board of
Accreditation, New Delhi for 3 years w.e.f. July,
2019.

OFFICIAL CORRESPONDENCE ADDRESS

The Coordinator,
AICTE sponsored STTP on Deep learning, Machine
learning and Pattern recognition,
EC Engineering Department,
Sarvajanik College of Engineering and Technology
(SCET),

Dr. R. K. Desai Marg, Opp. Mission Hospital,
Athwalines, Surat 395001, Gujarat, India

Contact No. for registration

9909152281 (Prof. Chintan Panchal)

9979931867 (Prof. Mrugesh Patel)

Contact No. for accommodation

7874359882 (Prof. Vandana Shah)

9998385988 (Prof. Bhargav Shah)

E-mail ID: sttp.dmp@scet.ac.in

ABOUT INSTITUTE – SCET

Sarvajanik Education Society has been an emblem of quality and value education for more than ten decades. Sarvajanik College of Engineering and Technology takes pride in being a part of this elite and noble organization serving for betterment of the society and the nation by imparting one of the basic needs of citizens for its survival i.e. EDUCATION.

This prestigious institute right from its very inception has been engaged in imparting quality technical education in nine different functionally specialized streams of engineering namely, Civil, Computer, Electronics and Communication, Information Technology, Instrumentation and Control, Electrical, Chemical, Mechanical and Textile Technology Engineering.

In past, the teaching institutes lacked a viable link for exchanging information between the real world (industries) & academic knowledge (institutes). Establishing the link, SCET has been actively involved in imparting technical training to the personnel's of Surat Municipal Corporation, Reliance Industries, Gujarat Gas and ESSAR Steel to name few.

ABOUT THE DEPARTMENT

The department of Electronics and Communication Engineering is actively contributing in quality research particularly in Wireless Communication, Optical Communications, Communication System, VLSI and Embedded System design. The department has 16 faculty members actively involved in research and academic activities. Electronics and Communication Engineering department is Re-Accredited by National Board of Accreditation, New Delhi for 3 years w.e.f. July, 2019.

PATRON

Dr. Hiren Patel, Principal, SCET

ADVISOR

Dr. Niteen Patel

COORDINATOR

Dr. Chirag Paunwala

ORGANISING CHAIR

Dr. Maulin Joshi

ORGANISING COMMITTEE

Prof. Aarohi Vora

Prof. Bhargav Shah

Prof. Chhaya Suratwala

Prof. Chintan Panchal

Prof. Dhiren Bhagat

Prof. Ketki Pathak

Dr. Mita Parikh

Prof. Mrugesh Patel

Dr. Nehal Shah

Prof. Neeta Chapatwala

Prof. Nirali Nanavati

Prof. Pritesh Saxena

Prof. Sarosh Dastoor

Prof. Tejal Joshi

Prof. Vandana Shah

Dr. Vivaksha Jariwala

SPEAKERS

The speakers will be from IIT, NIT and other reputed institutions. Some of the confirmed speakers are:

Dr. Arnav Bhavsar, IIT Mandi

Dr. Ashish Phophalia, IIIT Vadodara

Dr. Priyanka Sharma, Nirma University

Dr. Sridhar Swaminathan, Bennett University

VENUE

Electronics and Communication Engineering Department, Sarvajanik College of Engineering and Technology, Athwagate, Surat.

REGISTRATION FEES

There is no registration fee for first 40 participants. Pre-registration is mandatory. Last date of pre-registration is 15th September 2019. Selected candidates will receive registration number through email. All selected candidates are required to enclose a Demand Draft of Rs. 1000/- in favor of "Sarvajanik College of Engineering and Technology" payable at Surat. Mention the course name behind the Demand Draft. The demand draft will be refunded (first 40 participants) for the participants who will attend all sessions and successfully clear the exam at the end of program. Registration form should be send by speed post to the coordinator along with DD. Confirmation of selected candidates will be on first come first serve basis. The completed registration form should reach to the coordinator on or before 10th October 2019.

IMPORTANT DATES

Last date for Pre-registration: 30th September, 2019

Last date for Registration: 10th October, 2019

List of Eligible Candidates: 20th October, 2019
(E-mail will be sent to eligible candidates)

TA AND ACCOMMODATION

Participants from AICTE approved institutes will be given 3 tier AC fare (max. 3000/- including both sides) and will be given accommodation in hostel on twin sharing basis on prior confirmation only.